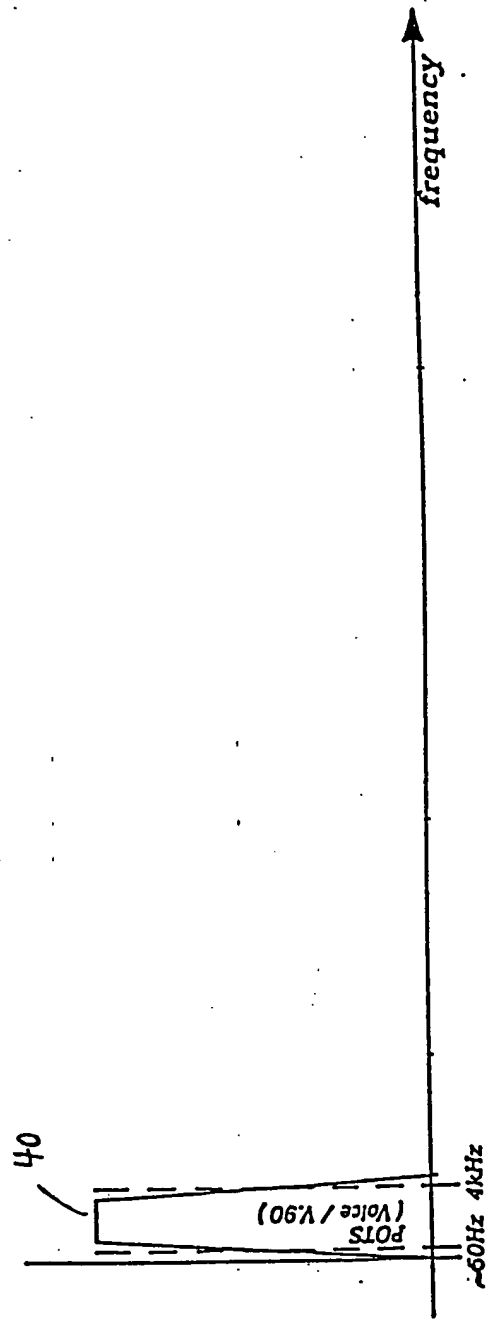
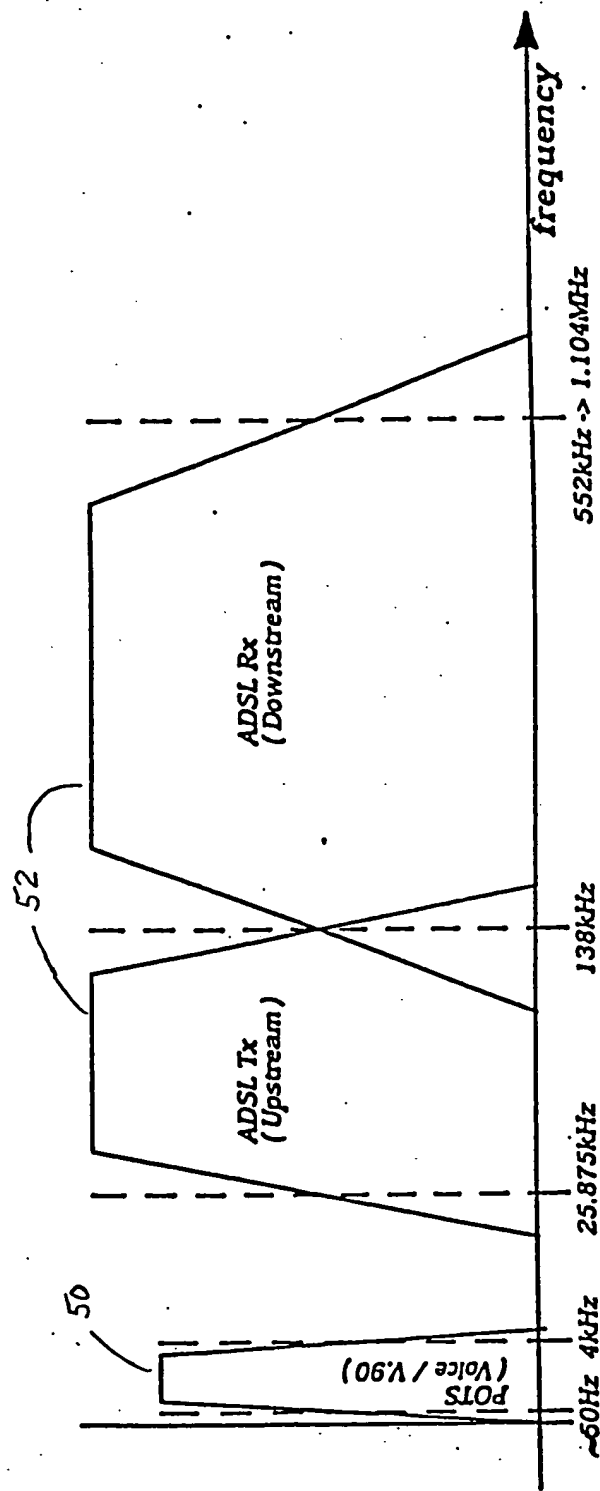


000000000000000000



Prior Art

Fig 1



Prior Art

Fig 2

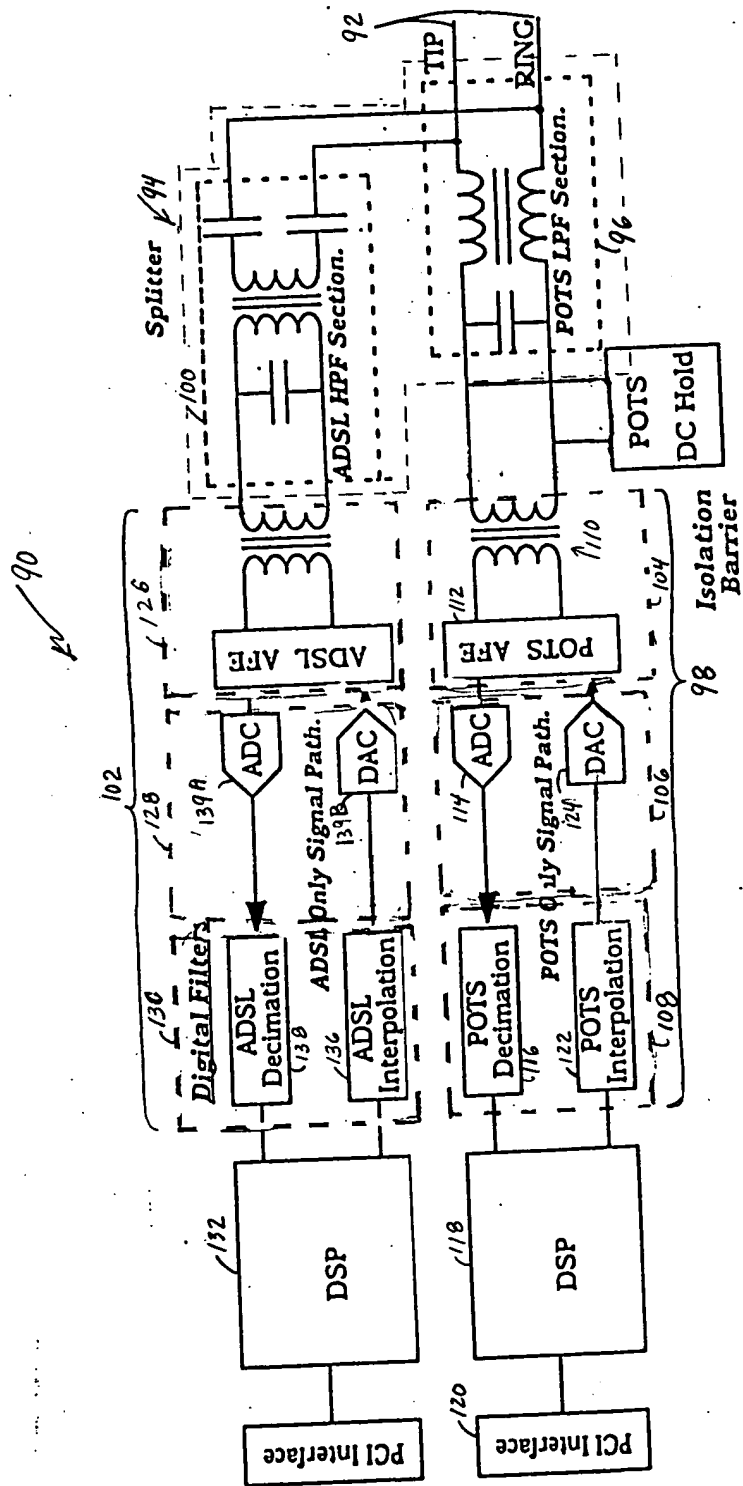


Fig. 3 Prior Art

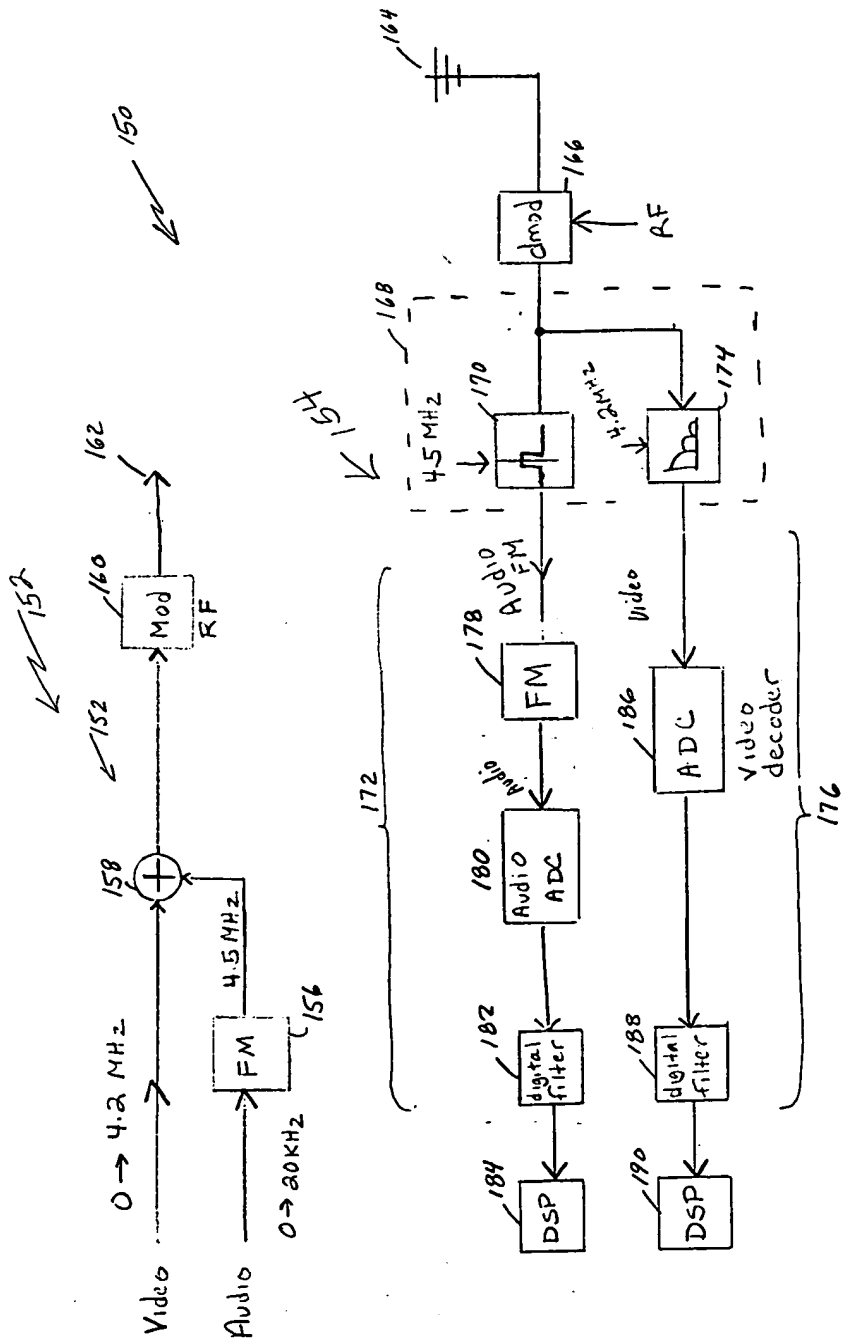


Fig 4 Prior Art

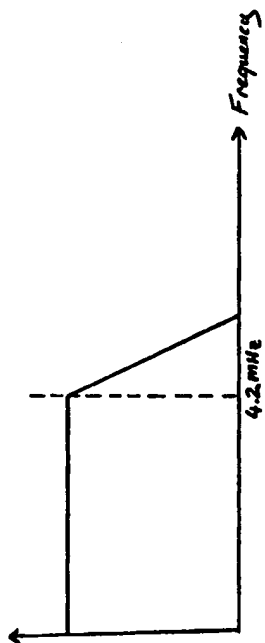


Fig. 5 Prior Art

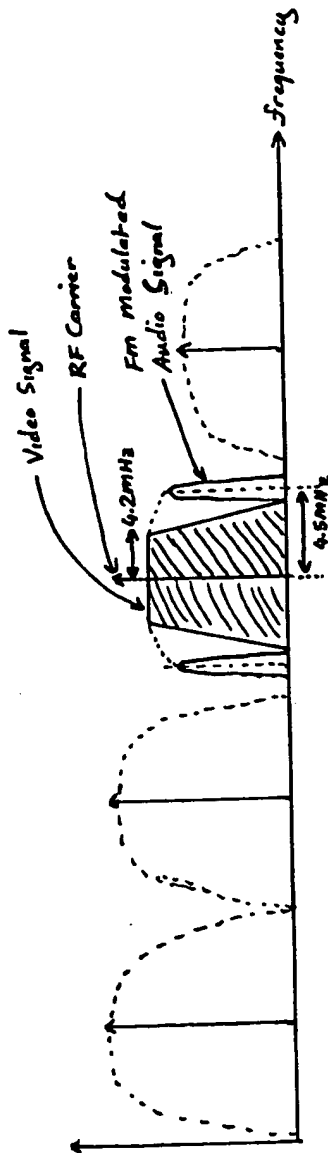


Fig. 6 Prior Art

**to Splitter / Phone Line**



Fig. 7

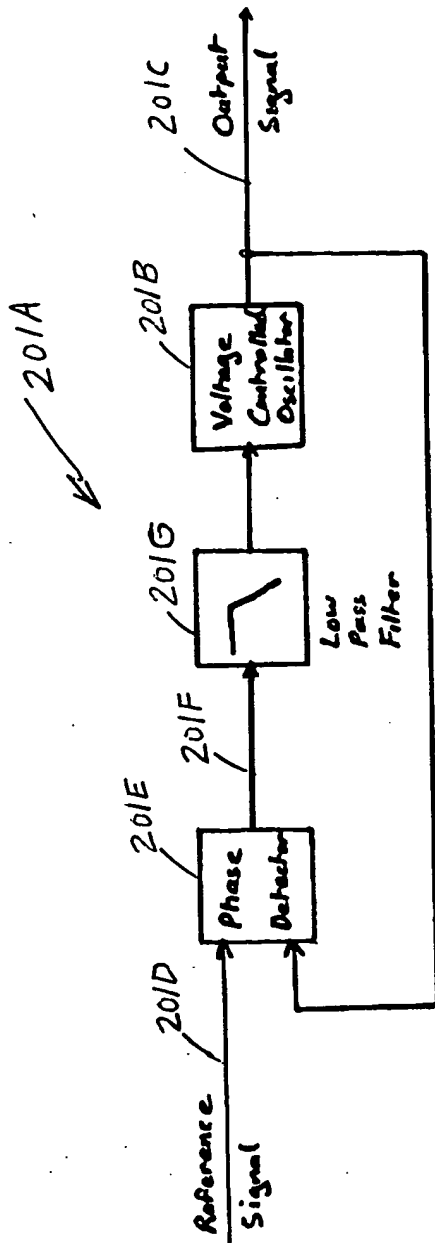


Fig. 8 Prior Art





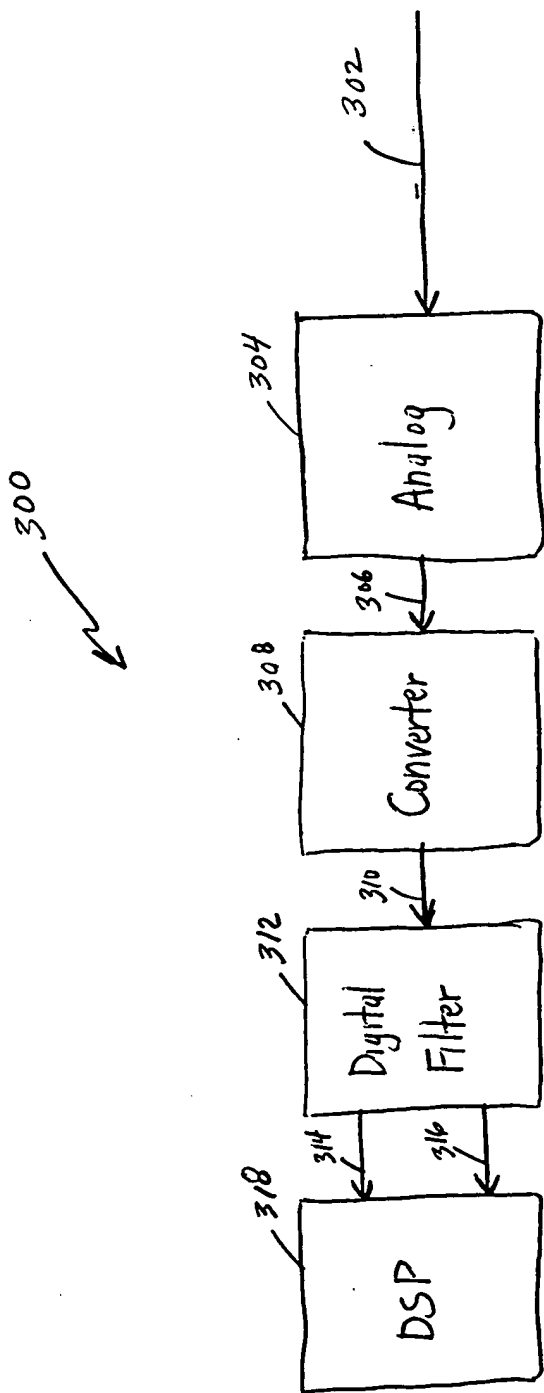


Fig. 10

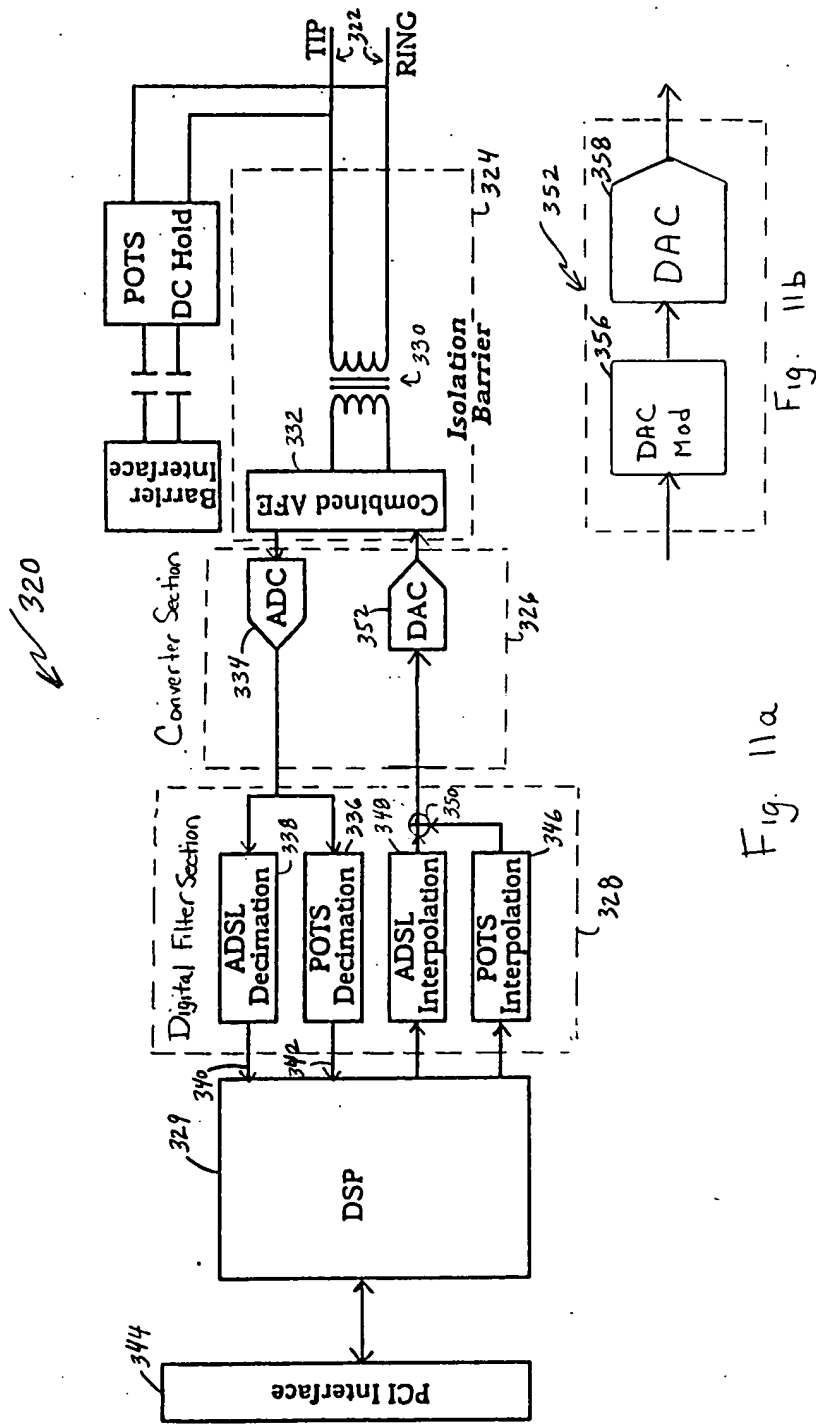
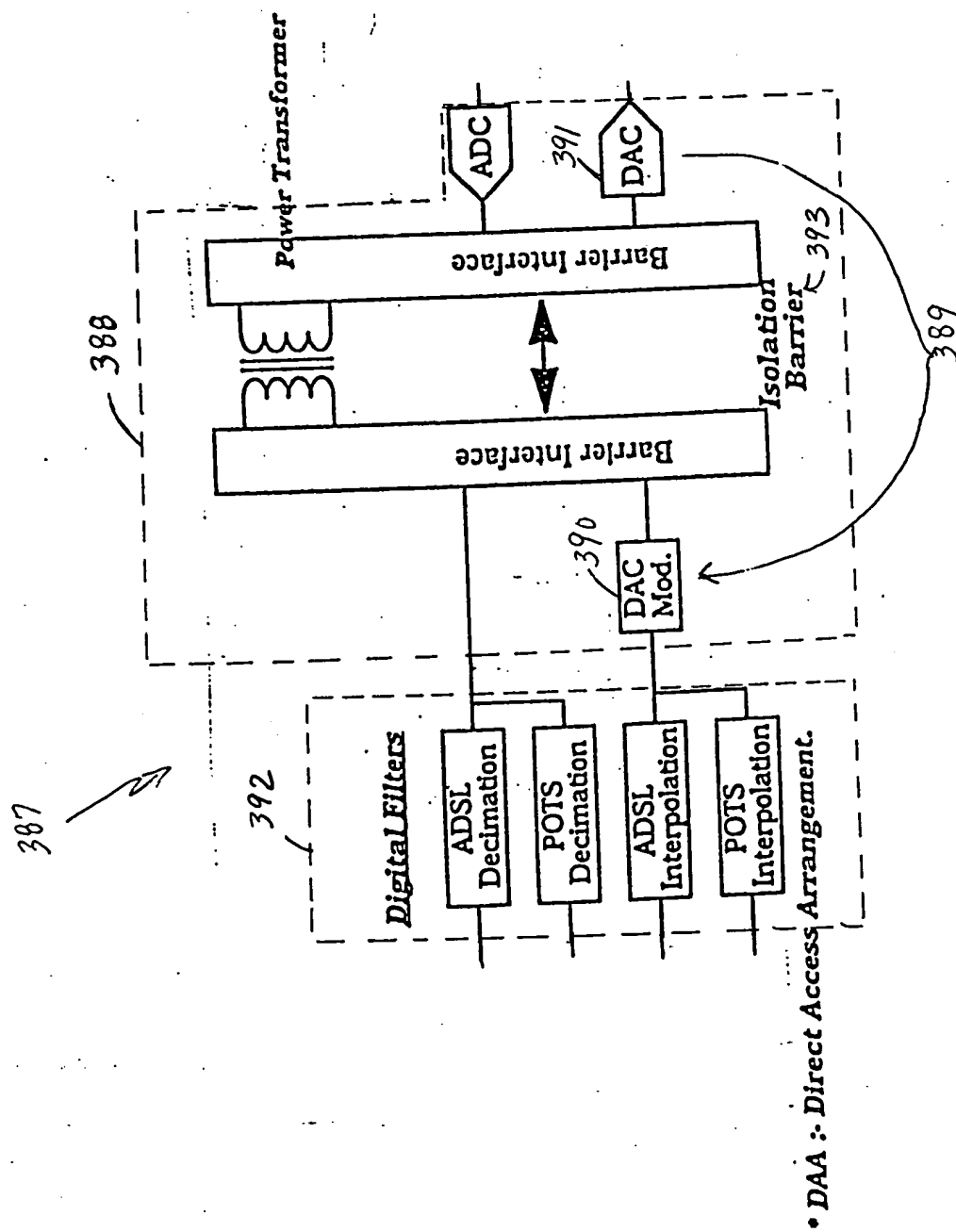


Fig. 11b





F<sub>19</sub>. 12b

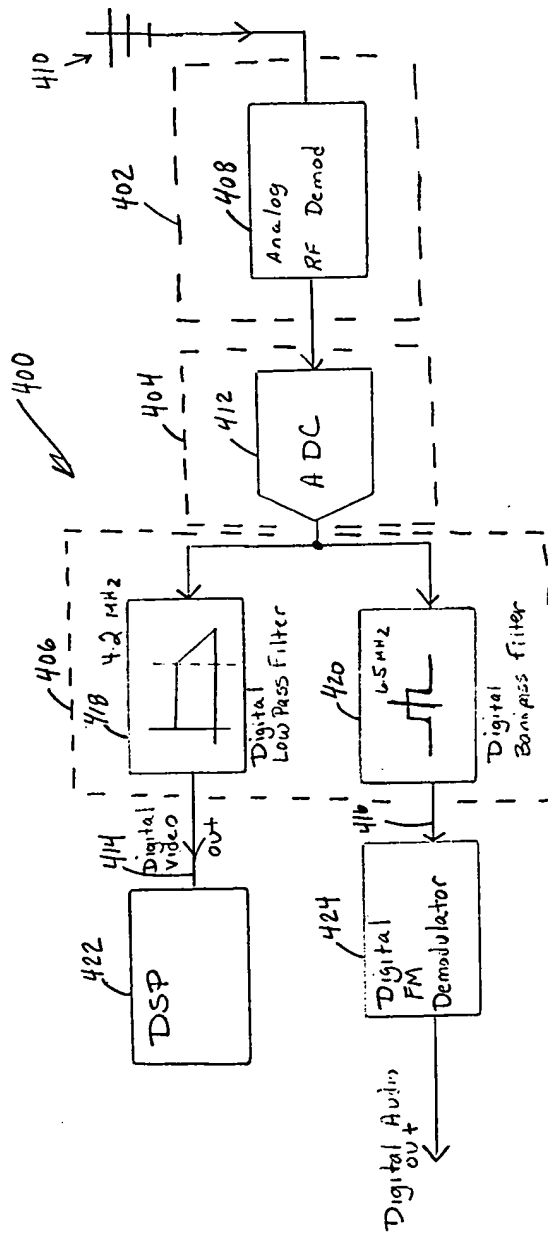


Fig. 13

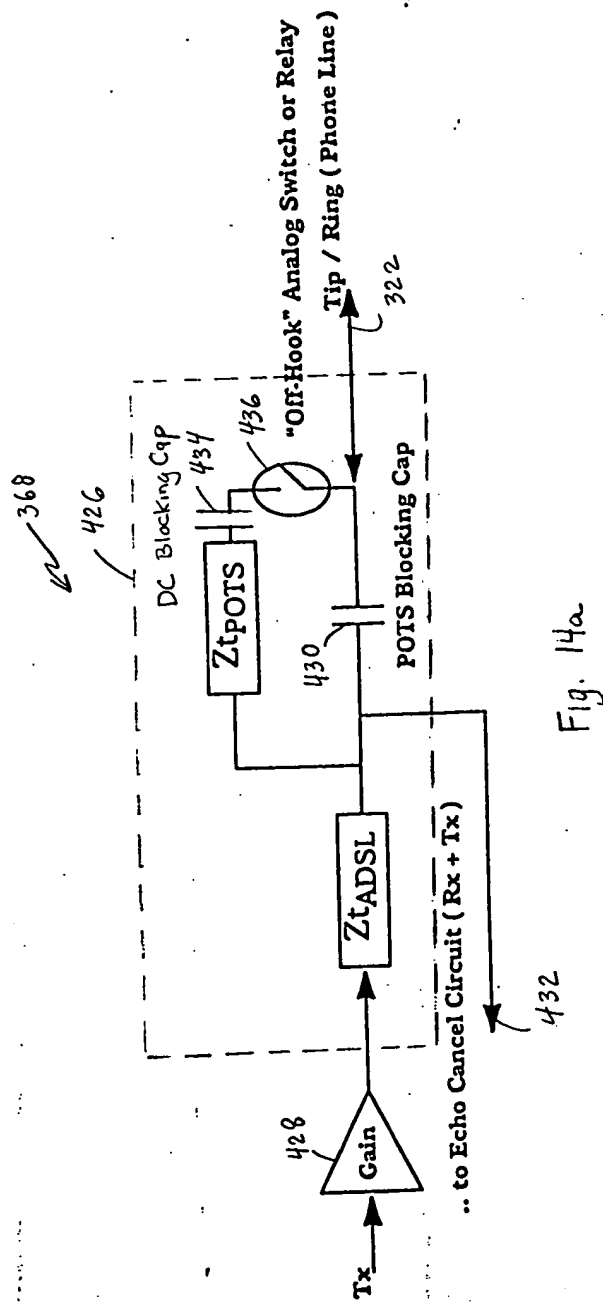


Fig. 14a

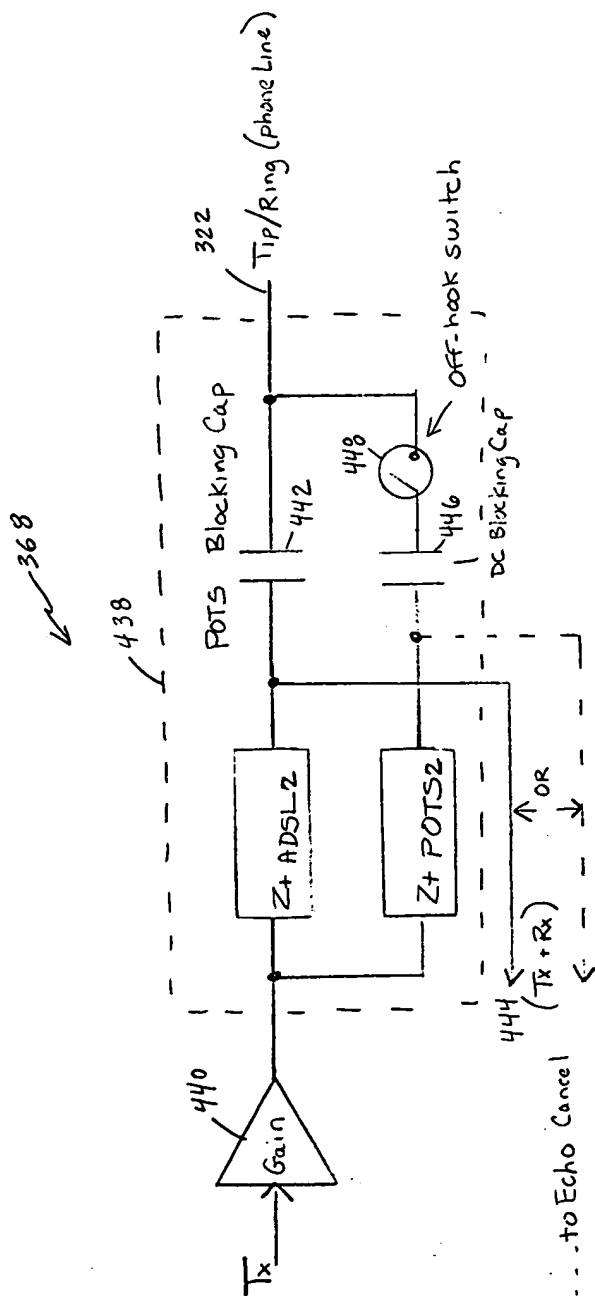


Fig 14b

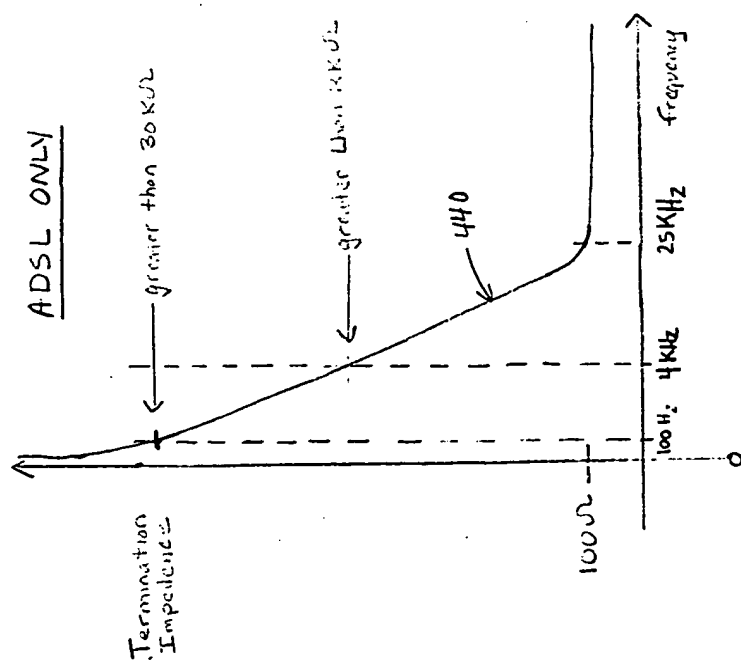
[illegible]

Fig. 15a

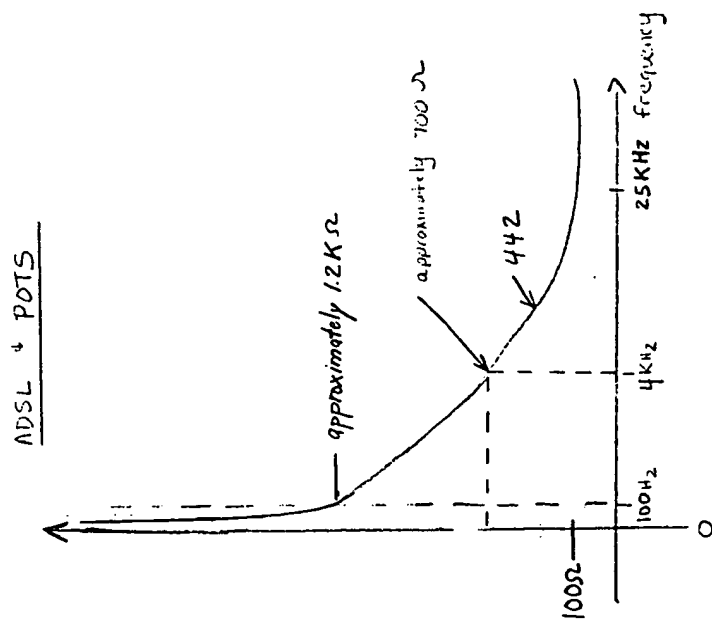


Fig. 15b



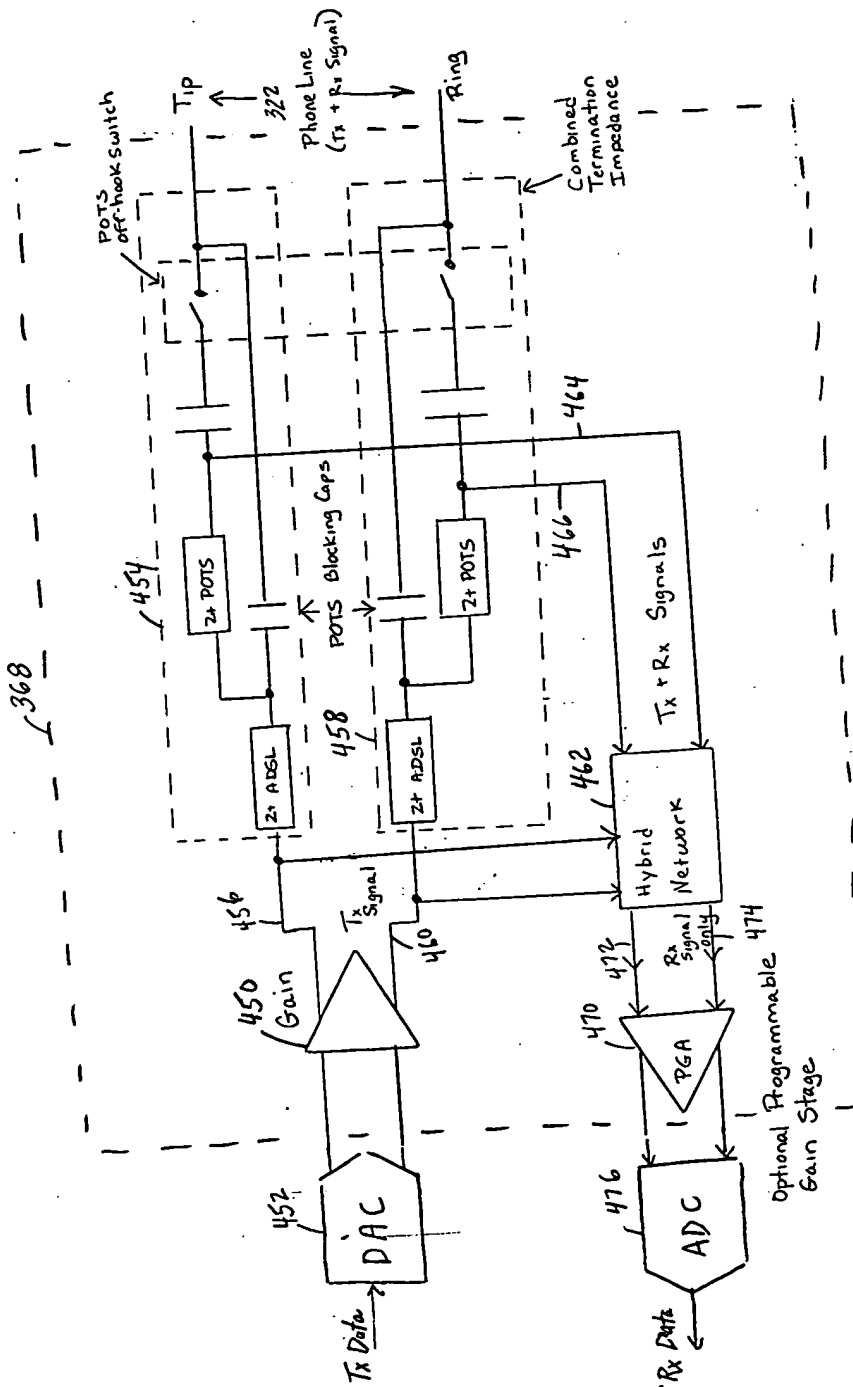


Fig 16a

FIG. 16b

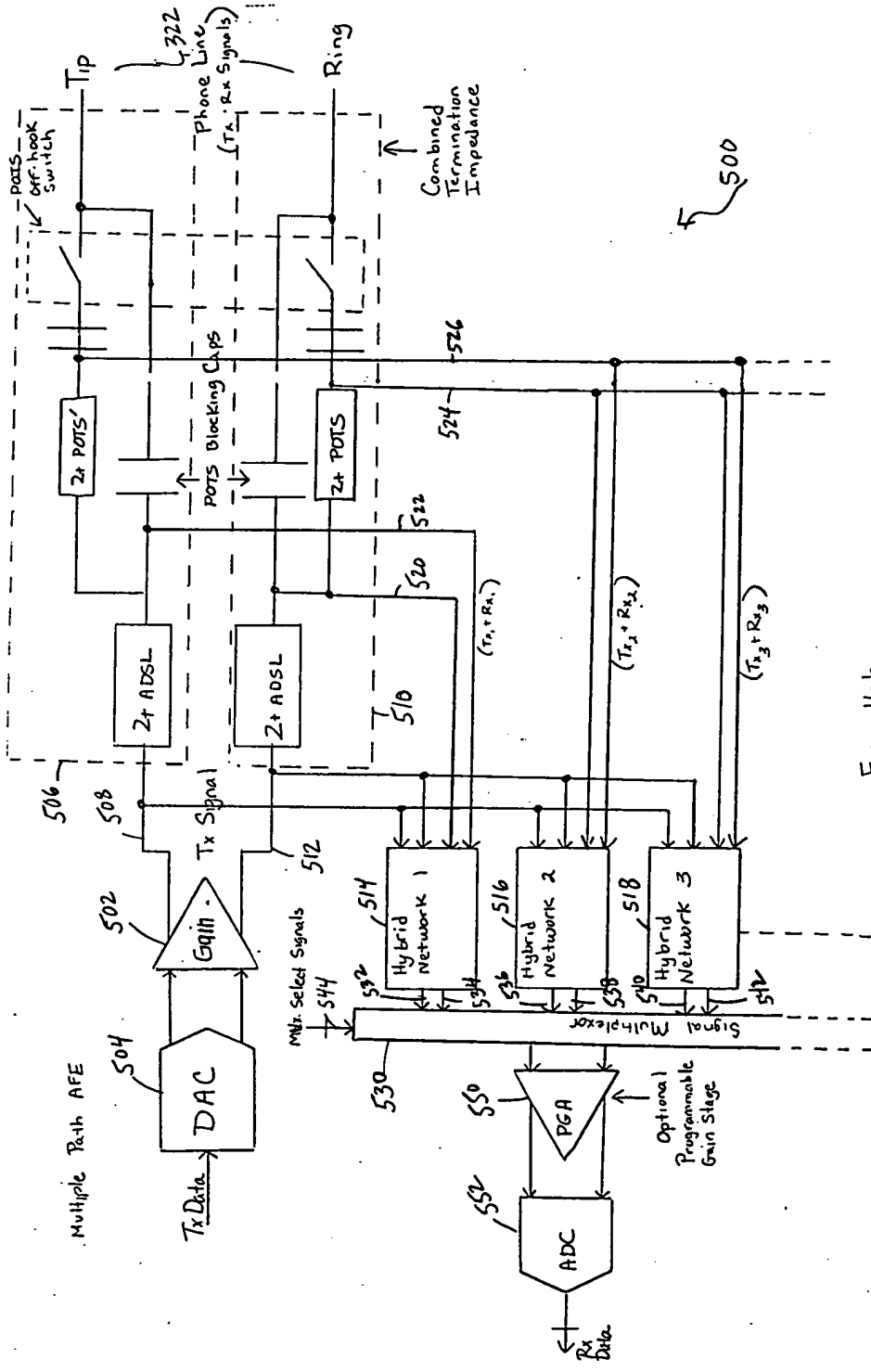


Fig 16b

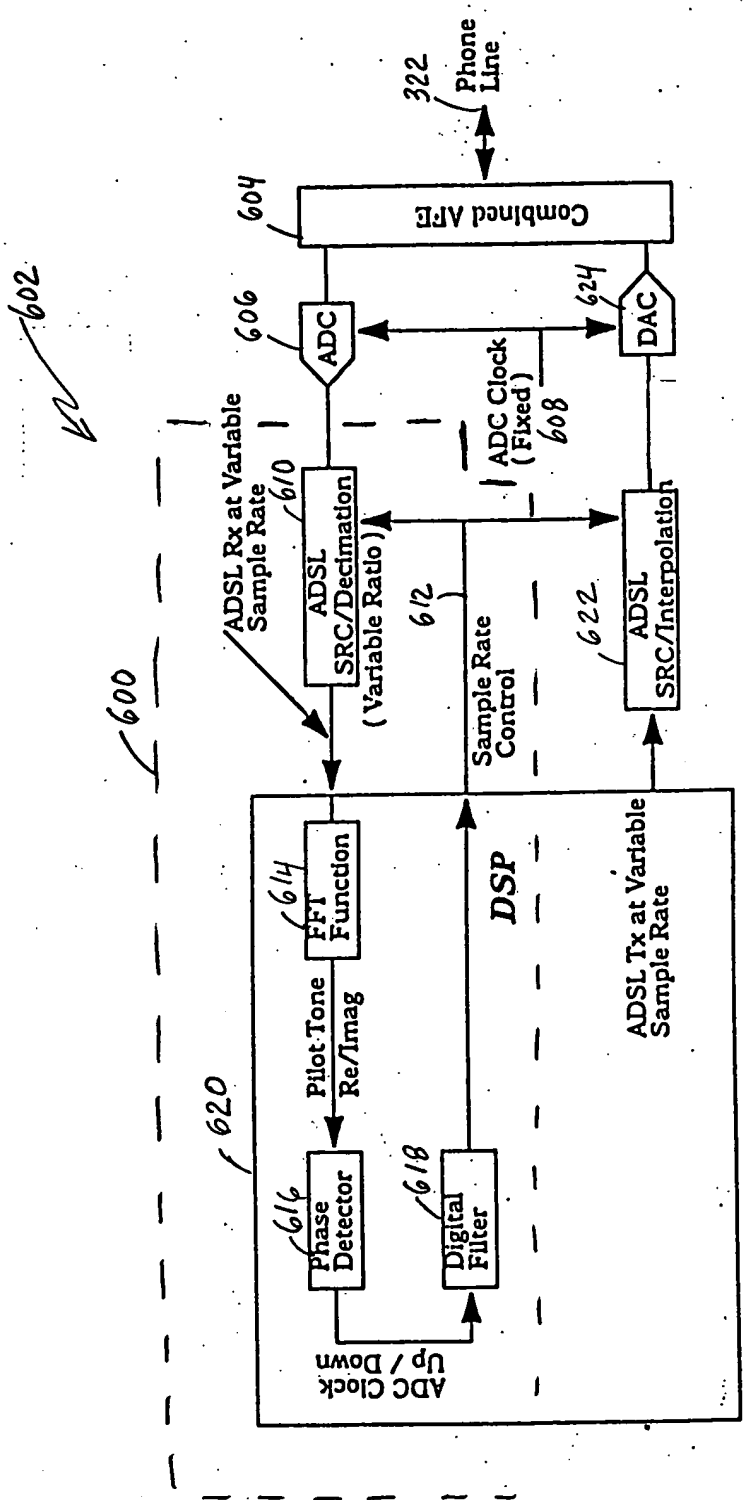


Fig. 17

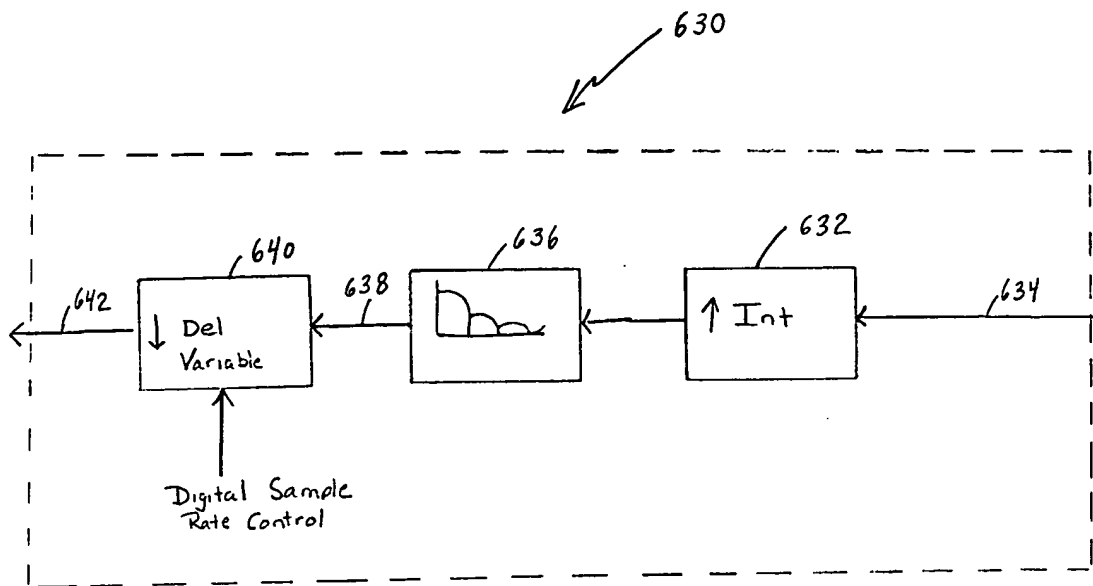


Fig 18

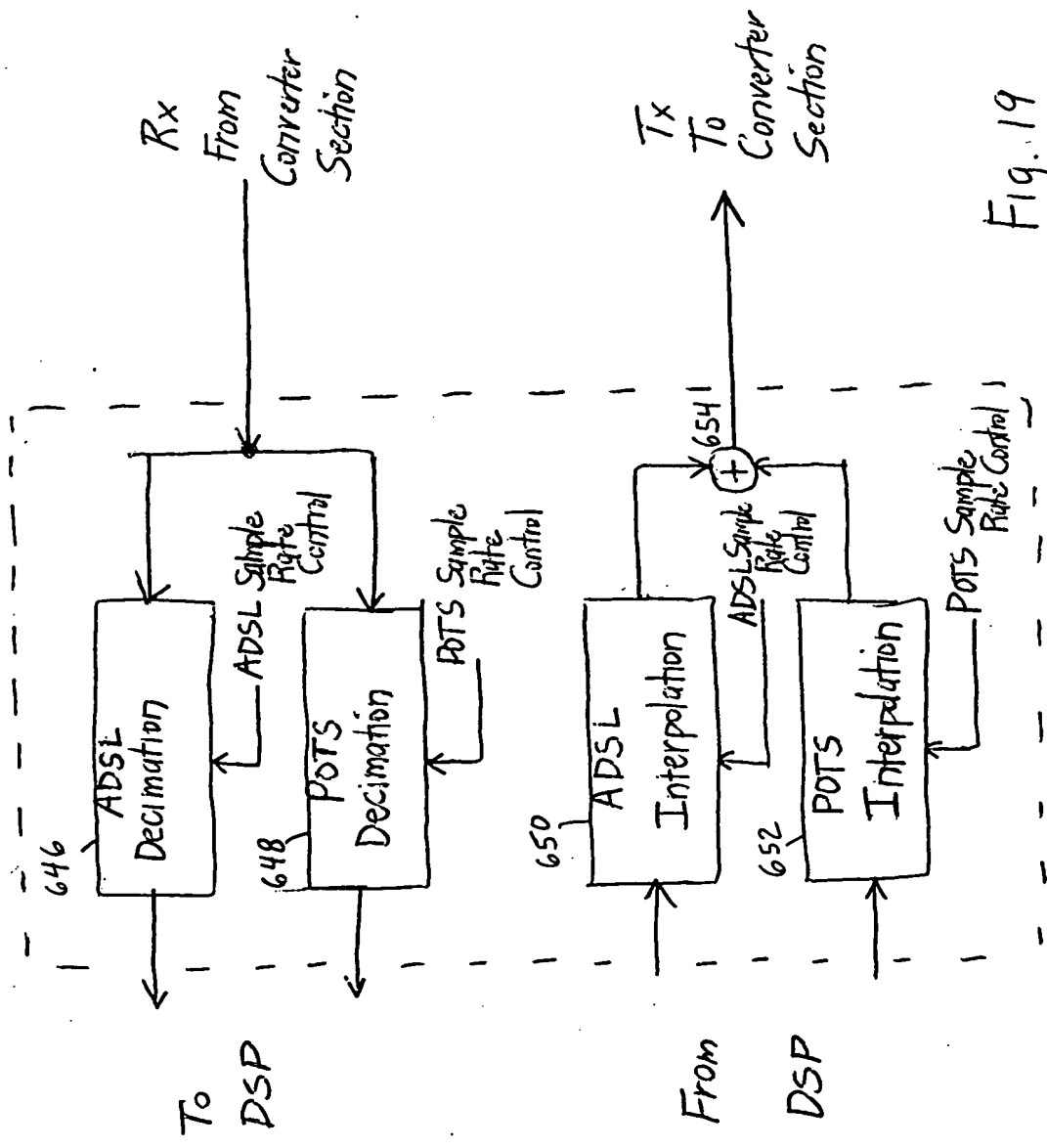


Fig. 19

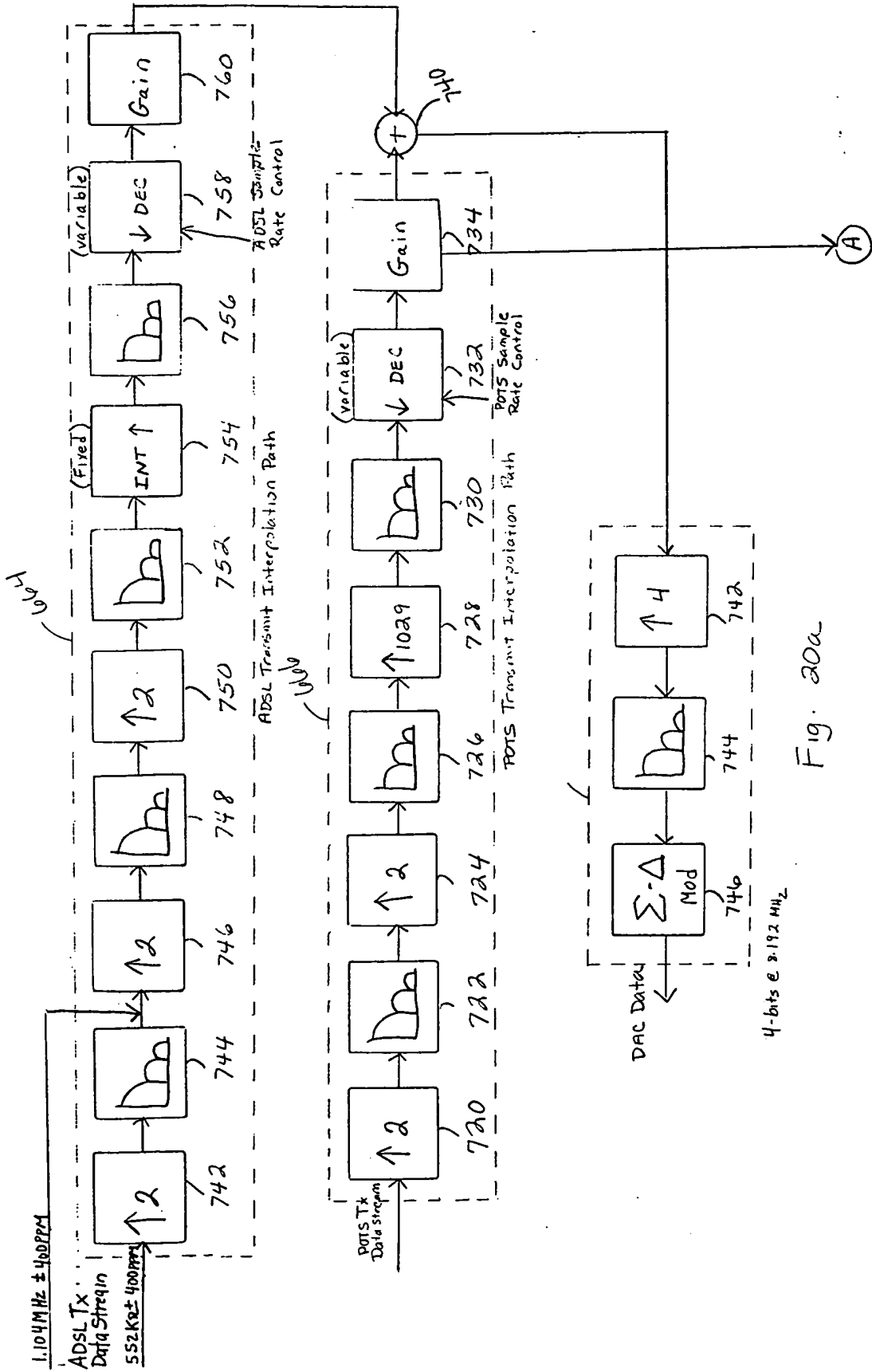


Fig. 20a

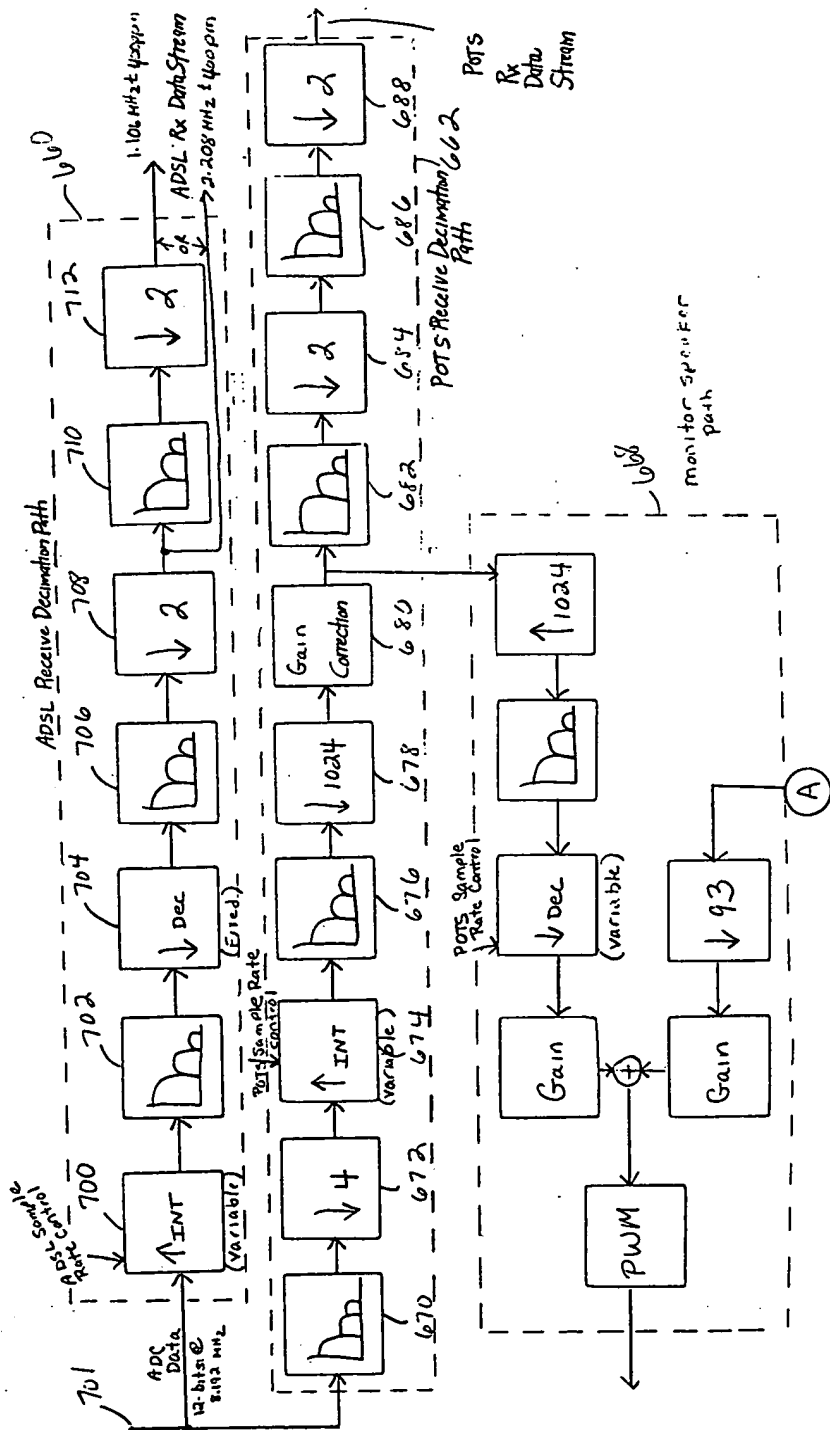
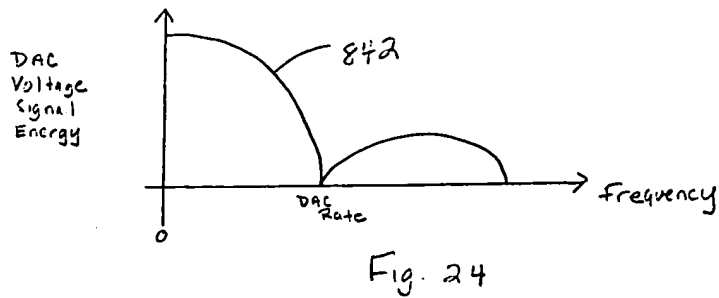
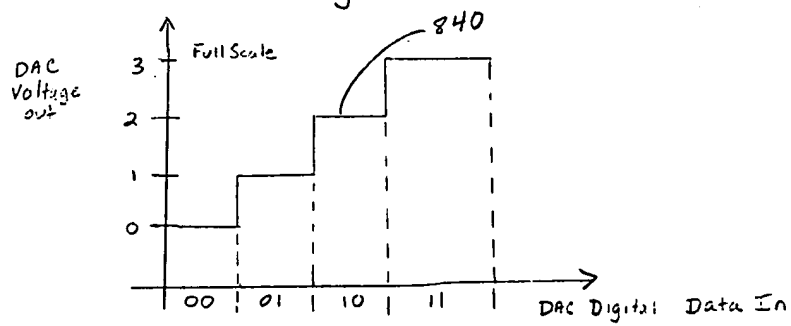
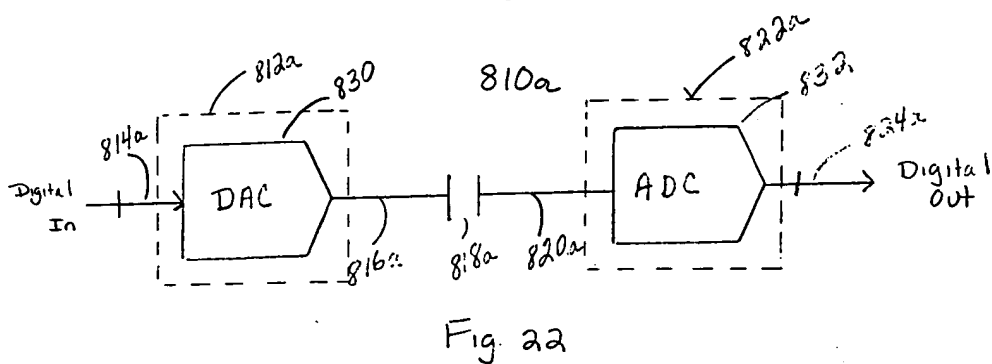
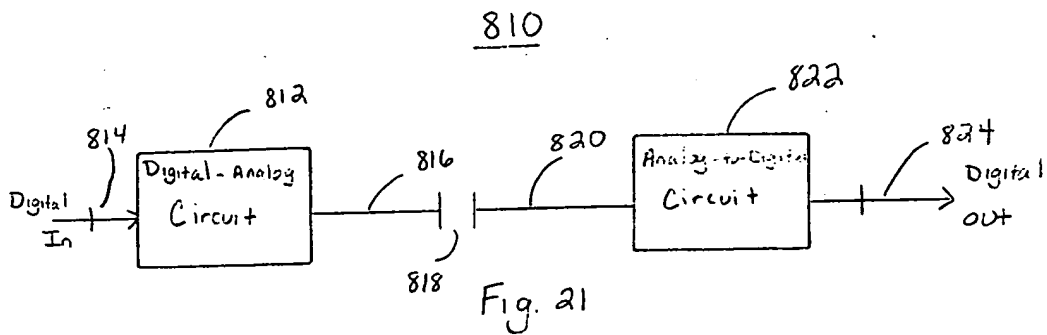


Fig 20 b





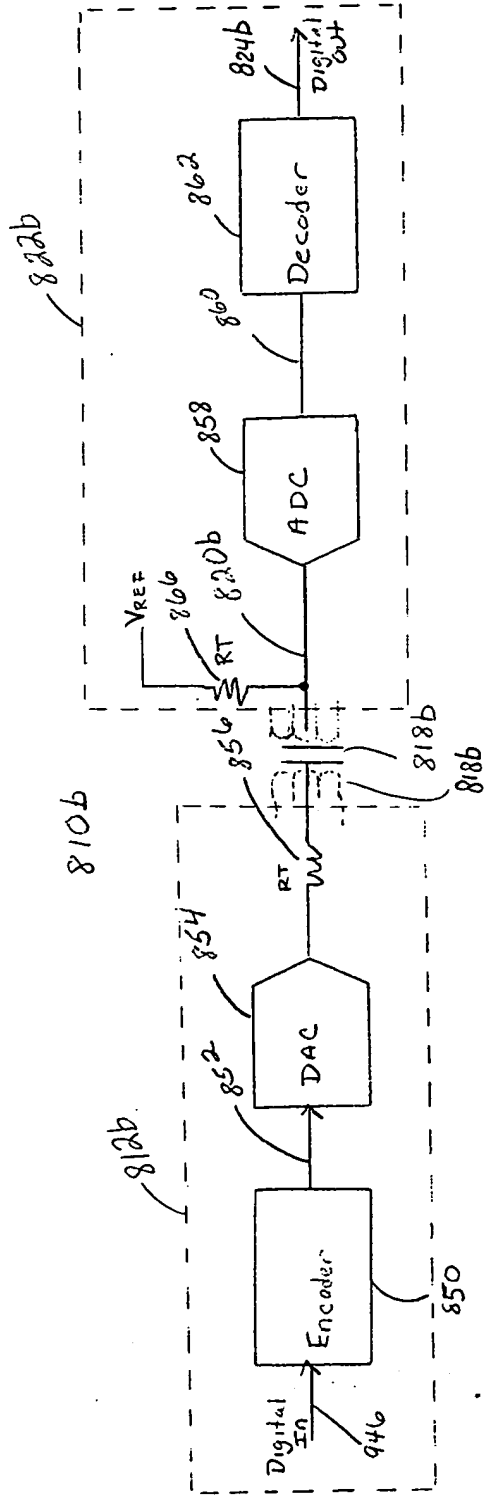


Fig. 25

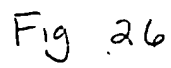
[illegible]

Fig. 26

DAC

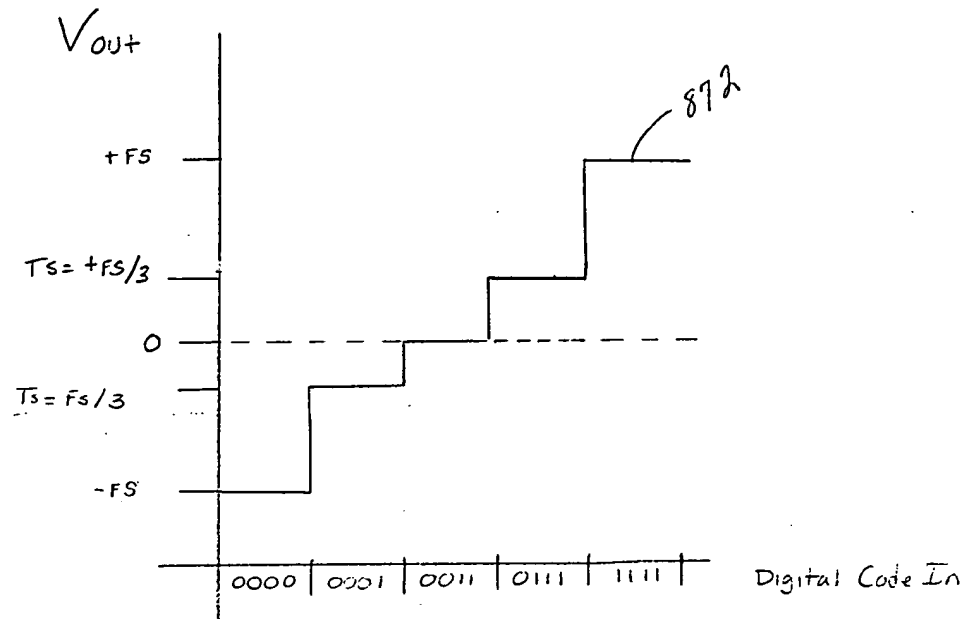


Fig. 27

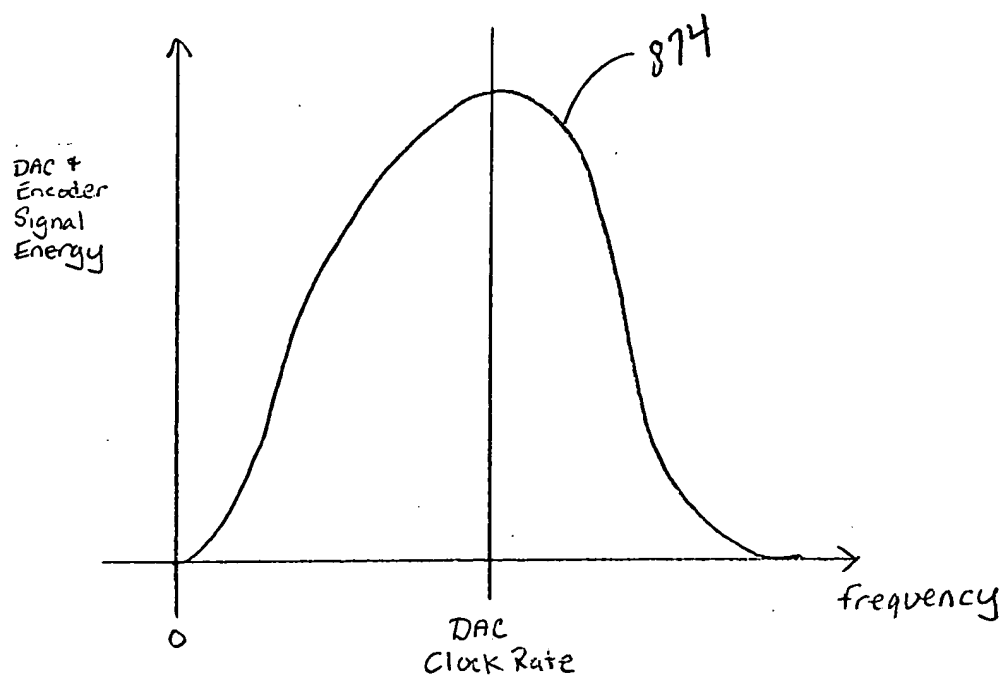


Fig. 28

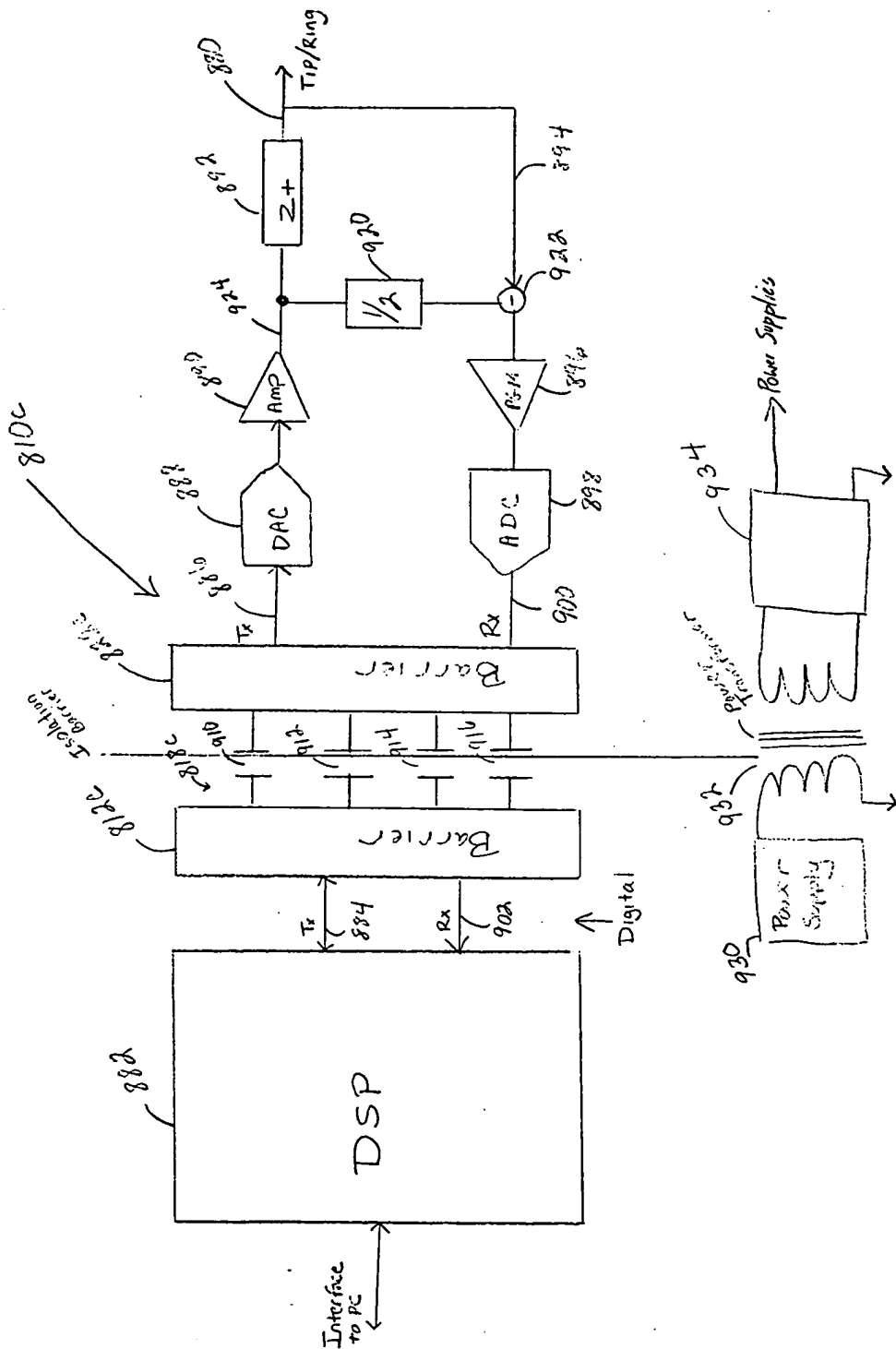


Fig. 29

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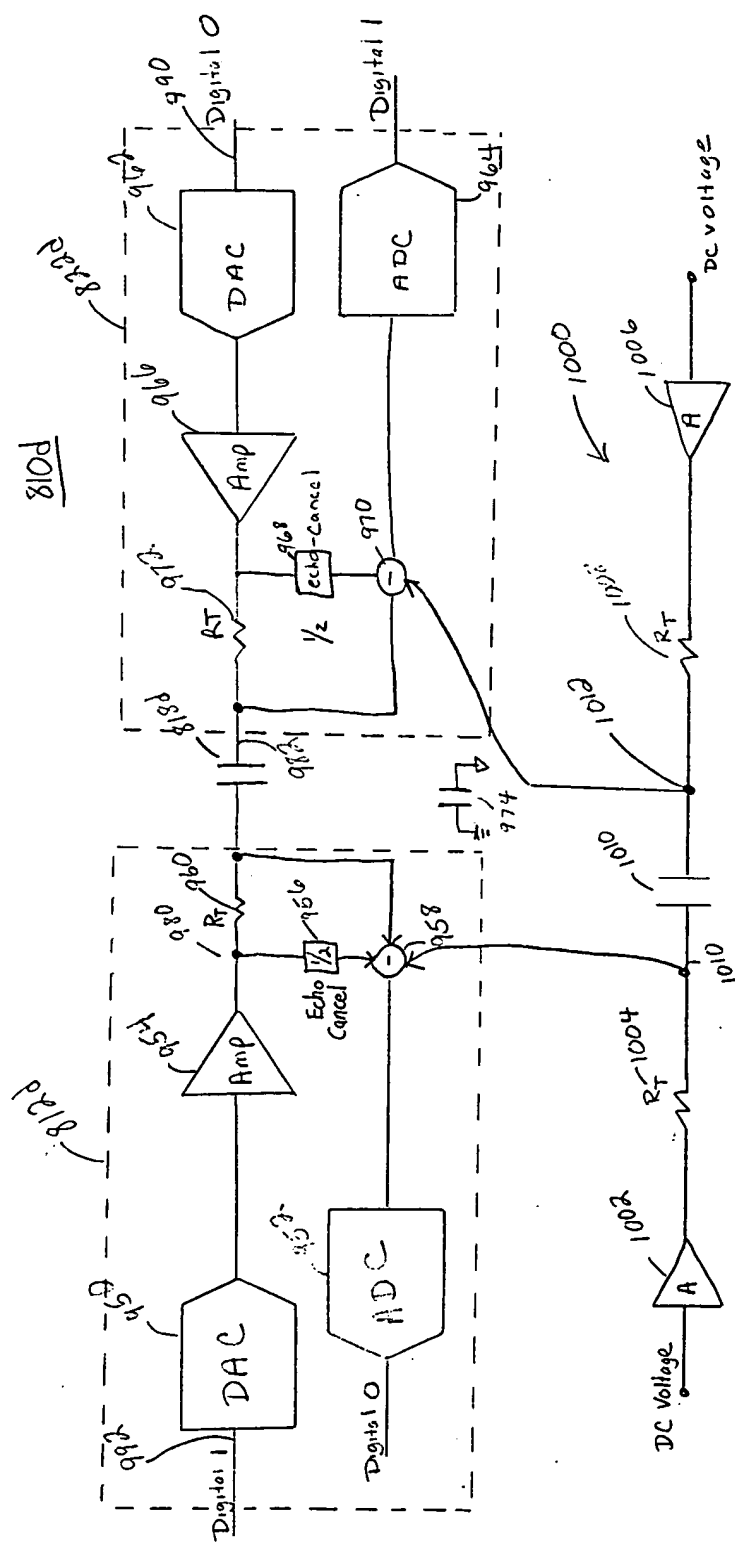


Fig 30

# Full Barrier System

810e

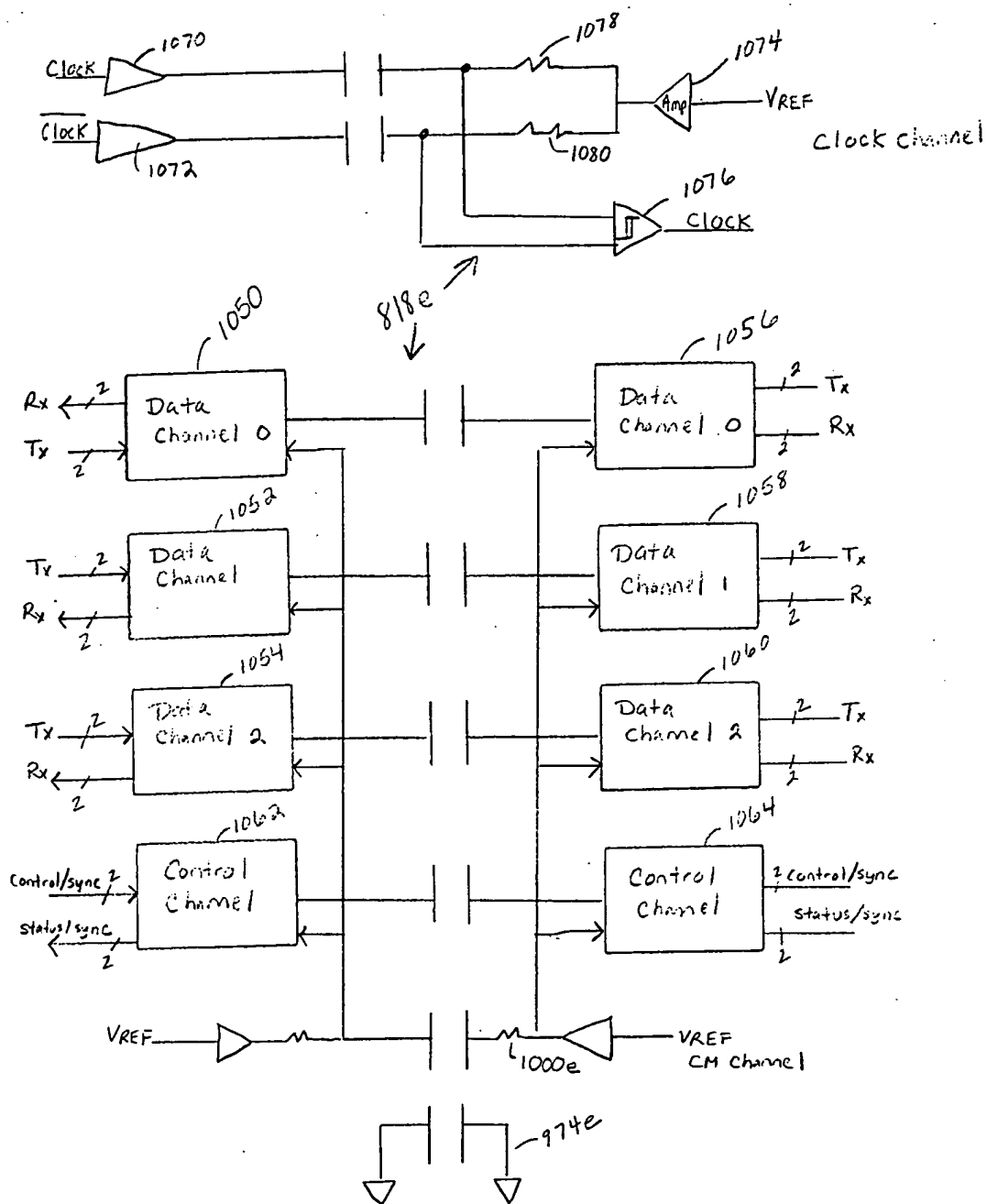


Fig. 31

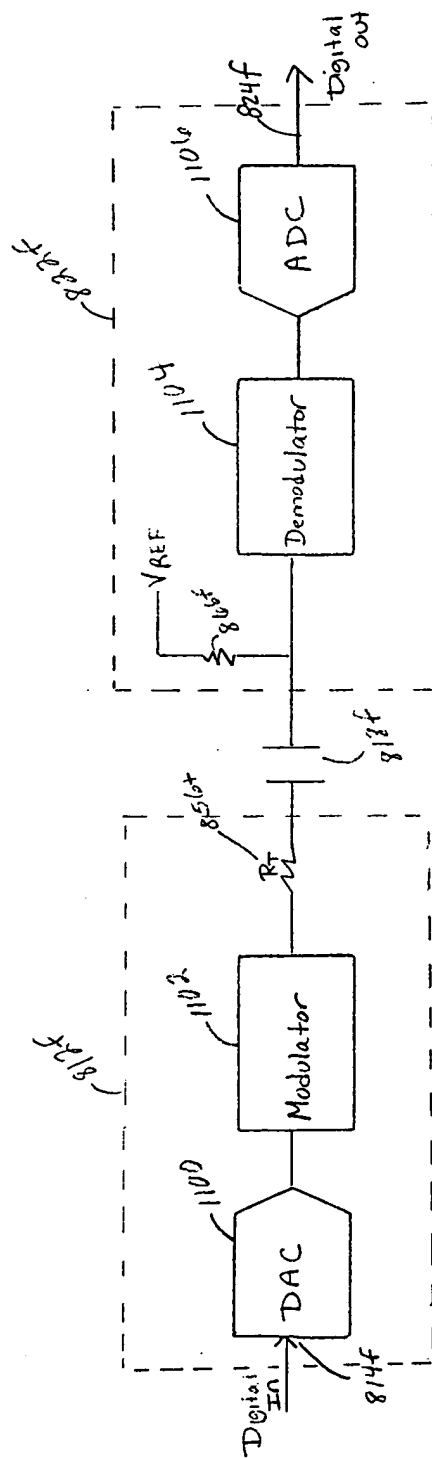


Fig. 32